

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	5	((circuit adj board) and semiconductor and bump\$ and adhesive and resin) and electro\$	USPAT; JPO	OR	OFF	2001/09/17 11:55
		((((printed adj circuit adj board) and support) and rail\$) and rod\$	USPAT; JPO	OR	OFF	2001/01/16 07:15
		(US-3826483-\$ or US-3930644-\$ or US-5694843-\$ or US-5707052-\$ or US-5899446-\$).did.	USPAT	OR	ON	2005/04/06 11:05
		(position\$3 adj device) with PCB	USPAT; JPO; DERWENT	OR	ON	2005/04/06 11:06
		2 and @ad<"19990901"	USPAT; JPO; DERWENT	OR	ON	2005/04/06 11:06
L4	6	("3889936"   "4159505"   "4976415"   "5711065"   "6161811"   "6251493").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/04/06 11:08
L5	2	("4980971"   "5084959").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/04/06 11:11
S1	22614	"269"/\$.ccls.	USPAT	OR	OFF	2001/09/18 11:37
S2	7	"269"/\$.ccls. and (printed adj circuit adj board adj support)	USPAT; JPO	OR	OFF	2001/01/16 06:01
S3	37	(printed adj circuit adj board adj support) and rail\$	USPAT; JPO	OR	OFF	2001/01/16 06:04
S4	293	printed adj circuit adj board adj support	USPAT; JPO	OR	OFF	2001/01/16 07:12
S5	62275	printed adj circuit adj board	USPAT; JPO	OR	OFF	2004/05/11 14:19
S6	20034	(printed adj circuit adj board) and support	USPAT; JPO	OR	OFF	2001/01/16 07:13
S7	2274	((printed adj circuit adj board) and support) and rail\$	USPAT; JPO	OR	OFF	2001/01/16 07:14
S8	479	((((printed adj circuit adj board) and support) and rail\$) and rod\$) and (block or table)	USPAT; JPO	OR	OFF	2001/01/16 08:14
S9	53	peckham.in.	USPAT	OR	OFF	2001/01/16 07:42
S10	7	peckham.in. and mark	USPAT	OR	OFF	2001/01/16 07:41
S11	0	teitenberg.in.	USPAT	OR	OFF	2001/01/16 07:41
S12	0	meridian.in.	USPAT	OR	OFF	2001/01/16 07:42
S13	1686	tony.in.	USPAT	OR	OFF	2001/01/16 07:42
S14	0	tony.in. and teitenberg	USPAT	OR	OFF	2001/01/16 07:42
S15	0	peckham.in. and (printed adj circuit adj board adj support)	USPAT	OR	OFF	2001/01/16 07:42

S16	0	tony.in. and (printed adj circuit adj board adj support)	USPAT	OR	OFF	2001/01/16 07:43
S17	233	269/903.ccls. and support	USPAT	OR	OFF	2001/01/16 08:25
S18	80	(269/903.ccls. and support) and (circuit adj board)	USPAT	OR	OFF	2001/01/16 08:25
S19	875	(circuit adj board) and semiconductor and bump\$ and adhesive and resin	USPAT; JPO	OR	OFF	2001/09/18 15:16
S20	3	"6034437".URPN.	USPAT	OR	OFF	2001/09/17 11:17
S21	3	"6034437".URPN.	USPAT	OR	OFF	2001/09/17 11:19
S22	8	("5352926"   "5523622"   "5703405"   "5786271"   "5834844"   "5844304"   "5886409"   "5886415").PN.	USPAT	OR	OFF	2001/09/17 11:19
S23	1	("6274389").PN.	USPAT; USOCR	OR	OFF	2004/05/11 14:19
S24	0	"6274389".URPN.	USPAT	OR	OFF	2004/05/11 14:19
S25	22	("4159976"   "4181645"   "4866108"   "5120665"   "5137936"   "5250848"   "5274913"   "5355580"   "5371328"   "5423931"   "5512613"   "5560934"   "5567266"   "5760337"   "5783867"   "5821456"   "5872158"   "5912282"   "5914381"   "5932682"   "5973033"   "6074895").PN.	USPAT	OR	OFF	2001/09/18 11:39
S26	922	29/271,289.1,760.ccls.	USPAT	OR	OFF	2004/05/11 14:19
S27	184141	PCB or (print\$2 adj circuit adj board)	USPAT; JPO; DERWENT	OR	ON	2004/05/11 14:20
S28	785	print\$2 adj circuit adj board adj support	USPAT; JPO; DERWENT	OR	ON	2004/05/11 14:20
S29	785	(PCB or (print\$2 adj circuit adj board)) and (print\$2 adj circuit adj board adj support)	USPAT; JPO; DERWENT	OR	ON	2004/05/11 14:20
S30	3	29/271,289.1,760.ccls. and ((PCB or (print\$2 adj circuit adj board)) and (print\$2 adj circuit adj board adj support))	USPAT; JPO; DERWENT	OR	ON	2004/05/11 14:21
S31	1	(US-6412768-\$.did.	USPAT	OR	OFF	2004/05/11 14:21
S32	1	((US-6412768-\$.did.) and (contact adj surface)	USPAT; JPO; DERWENT	OR	ON	2004/05/11 15:15

S33	1	(29/271,289.1,760.ccls. and ((PCB or (print\$2 adj circuit adj board)) and (print\$2 adj circuit adj board adj support))) and (contact adj surface)	USPAT; JPO; DERWENT	OR	ON	2004/05/11 14:22
S34	1	((US-6412768-\$).did.) and (positioning adj surface\$1)	USPAT; JPO; DERWENT	OR	ON	2004/05/11 14:25
S35	17	"3930644".URPN.	USPAT	OR	ON	2004/05/11 14:41
S36	3	"5899446".URPN.	USPAT	OR	ON	2004/05/11 14:42
S37	39	(print\$2 adj circuit adj board adj support) and bias\$3 and stop	USPAT; JPO; DERWENT	OR	ON	2004/05/11 15:06
S38	1	((US-6412768-\$).did.) and (stop)	USPAT; JPO; DERWENT	OR	ON	2004/05/11 15:17
S39	0	((US-6412768-\$).did.) and (limit\$3)	USPAT; JPO; DERWENT	OR	ON	2004/05/11 15:17


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| <input type="checkbox"/> | <p>6. Using power supply current monitoring and P1149.4 for parametric testing of passive components<br/>Machado da Silva, J.; Silva Matos, J.;<br/>Circuits and Systems, 1997. ISCAS '97., Proceedings of 1997 IEEE International Symposium on<br/>Volume 4, 9-12 June 1997 Page(s):2713 - 2716 vol.4<br/><a href="#">AbstractPlus</a>   Full Text: <a href="#">PDF(344 KB)</a> IEEE CNF</p> |



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Naehyuck Chang; Kwan-Ho Kim; Heonshik Shin;  
TENCON 99. Proceedings of the IEEE Region 10 Conference  
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